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Q. 2.4: Reduce following Boolean expressions to the indicated number of literals (a) $A'C' + ABC + AC'$ Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates and an inverter.

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are $x = 3$ and $x = 6$. **computer system architecture morris mano lecture notes(chapter#9)**

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sequential circuit has two JK flip-flops A and B, two inputs x and y, and one output z

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Questions Ch.5 ?Q6, 7, 11, 12a, 13a, 16a, 19a (Digital Design Morris Mano) Dr Manohar re

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Krishnamurti - Ojai 1984 - Public Talk 3 - Attention is like a fire Q. 5.2: Construct a JK flip-

flop using a D flip-flop, a two-to-one-line multiplexer, and an inverter Exercise Solution -

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